

1/5

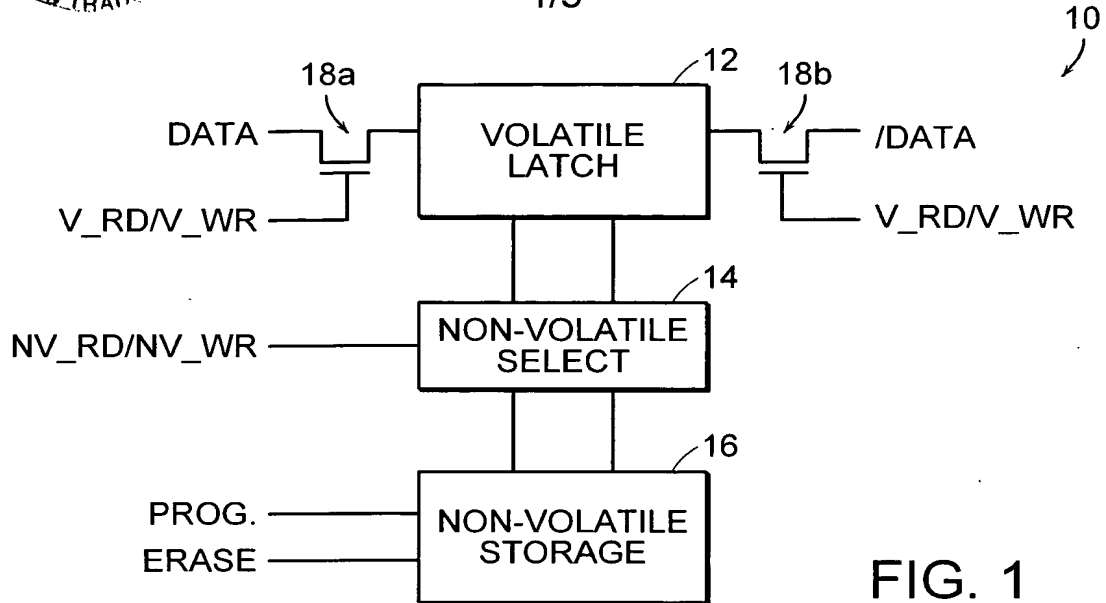


FIG. 1

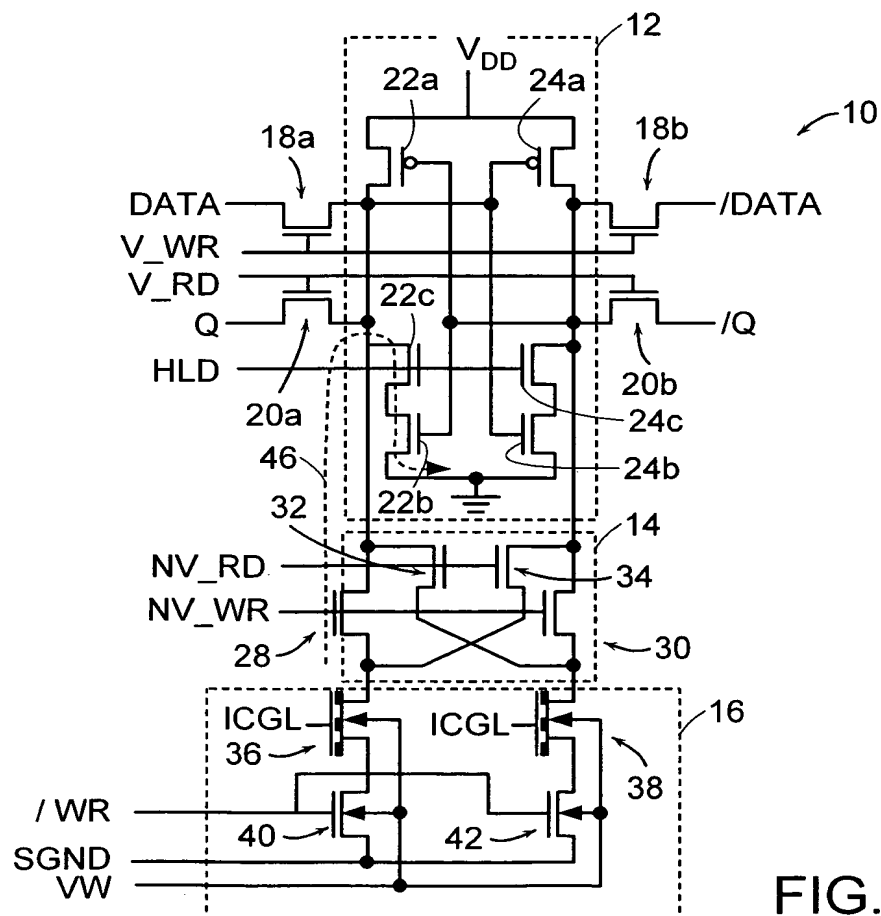


FIG. 2

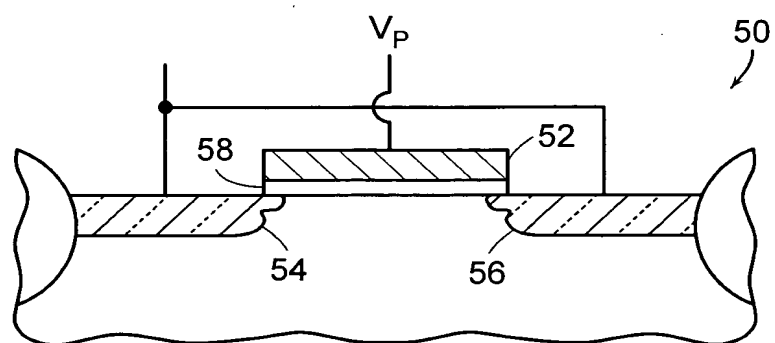


FIG. 3

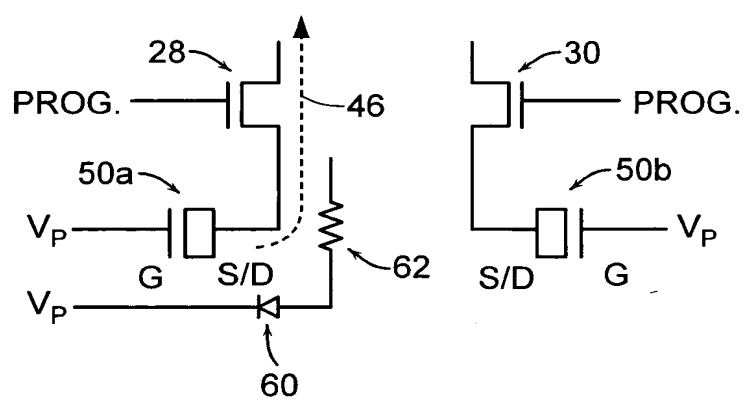


FIG. 4

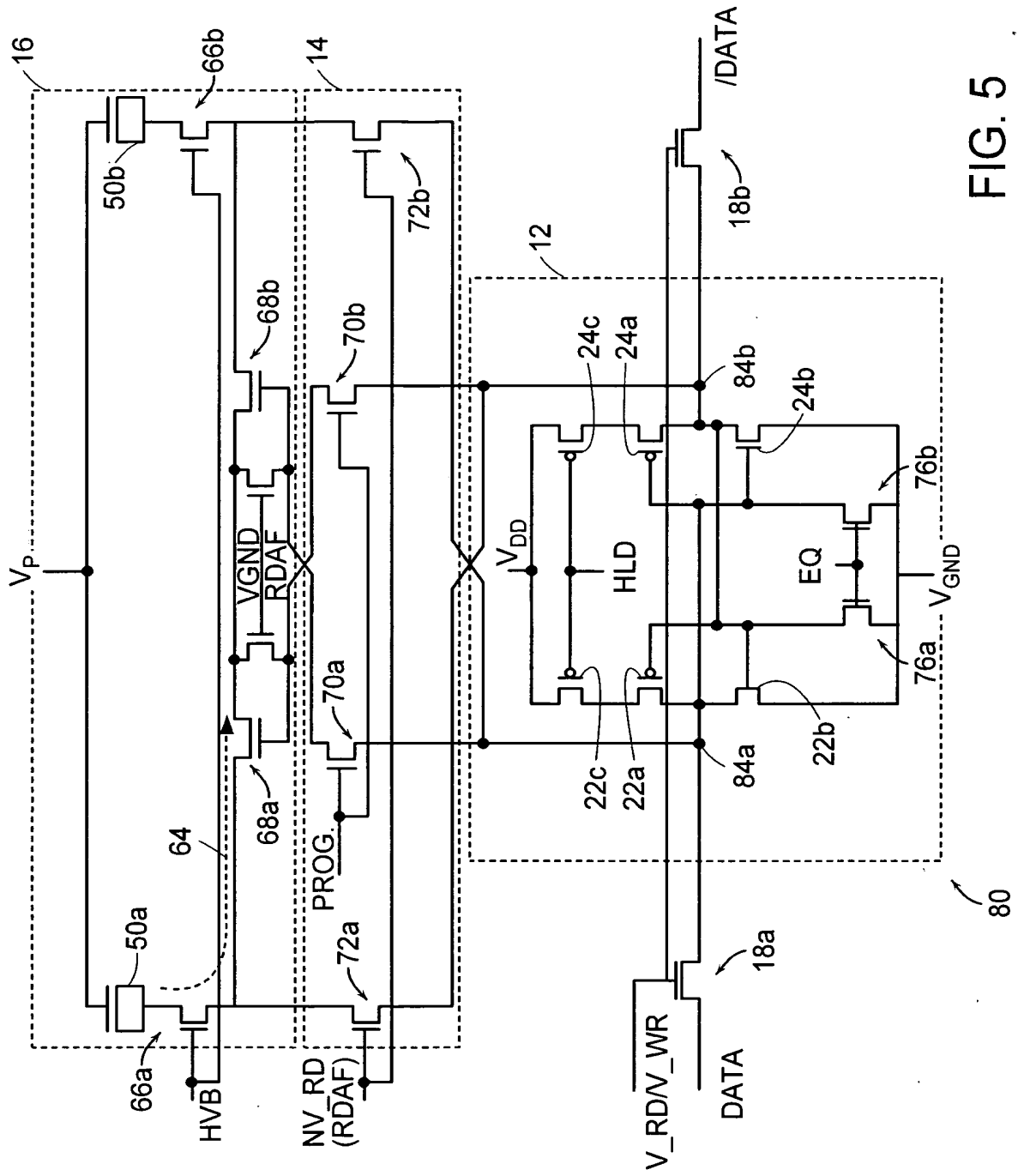
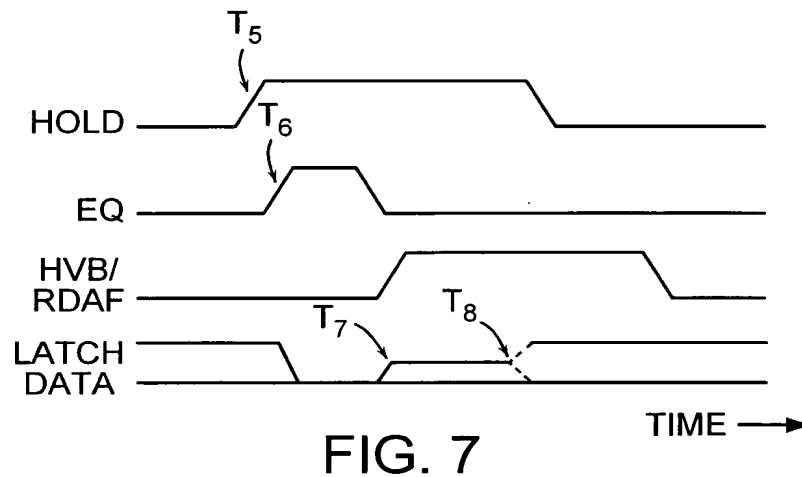
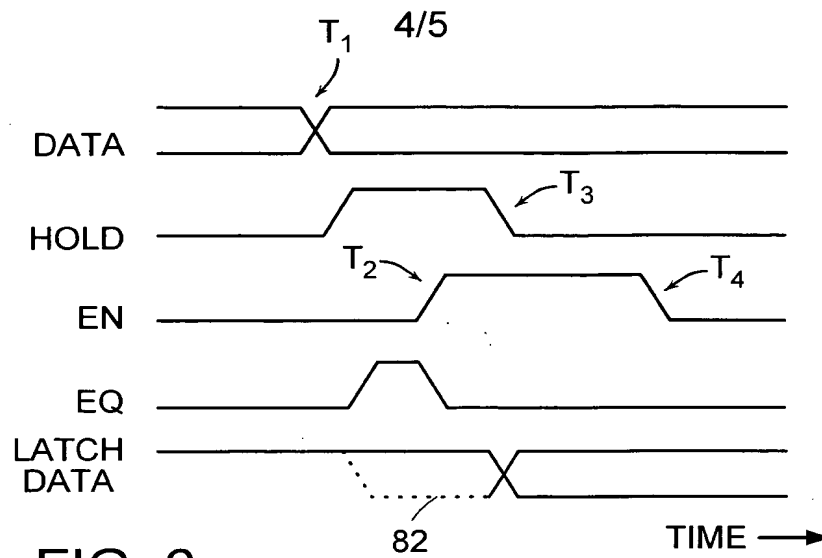


FIG. 5



	EN	HLD	NV_RD RDAF	PROG	HVB	V _P
IDLE (HOLD)	0	0	0	0	0	LOW
WRITE TO LTCH FROM D_BUS	1	1*0	0	0	0	LOW
PRGM AF FROM LTCH	0	0	0	1	1	HIGH
WRITE TO LTCH FROM AF	0	0	1	0	1	LOW

FIG. 8

